Title Block:

Title	eDIP USP Programmer			
Size	Document Number			REV
A	9777-1			C
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LCD1 — EDIP240-7

Pin	Signal		Pin	Signal
20	TEST		21	NC
19	EEPWP		22	NC
18	EEPSCL		23	NC
17	EEPSDA		24	NC
16	SUM	BUZZER	25	NC
15	SCL	SCL	26	NC
14	SDA	SDA	27	NC
13	DPOM	DPOM	28	NC
12	EN485	E485	29	NC
11	TXD5	TXD	30	NC
10	RXD5	RXD	31	NC
9	CLK	CLK	32	NC
8	BAUD1	MISO	33	NC
6	BAUD0	MOSI	34	NC
5	RESET	SS	35	NC
4	VOUT	RESET	36	NC
3	VADJ	VOUT	37	NC
2	VDD	VADJ	38	NC
1	GND	VDD	39	A
		GND	40	C

J4 / J5 — G-20

TEST 20, EEPWP 19, EEPSCL 18, EEPSDA 17, SUM 16, SCL 15, SDA 14, DPOM 13, EN485 12, TXD5 11, RXD5 10, CLK 9, BAUD2 8, BAUD1 7, BAUD0 6, RESET 5, VOUT 4, VADJ 3, VDD 2, GND 1

IC1 — FT232BM

TXD, RXD, RTS#, CTS#, DTR#, DSR#, DCD#, RI#, TXDEN, TXLED#, RXLED#, PWRCTL, PWREN#, SLEEP#, 3V3OUT, USBDM, USBDP, RSTOUT#, RESET#, XTIN, XTOUT, EBCS, EESK, EEDATA, TEST, AVCC, VCC, GND

IC2 — 93C46

CS, SK, DIN, DOUT, VCC, NC, NC, GND

IC3 — MAX202ECPE

C1+, C1-, C2+, C2-, V+, V-, T1, T2, R1, R2, TD, RTS, RD, CTS, TXD, RTS, RXD, CTS

IC4 — 75176

A, B, DE, RE, Y, GND, VDD, TXD5, EN485, RXD5

J3 — SG-10

DSR 1, DCD 2, CTS 3, TXD 4, RTS 5, RXD 6, DTR 8, V5, GND

T1 (T1B, T1C) — Summer SP1
T2 — IRLM6702

Other components:

- R1 10k, R2 2k2, R3 470, R4/R5 2x27, R6 1k5, R7 1k, R8 10k, R10 0R5, R11 10k, R12 1k, R13 10k
- C1 100n, C2 100n, C3 10uF, C4 33n, C5 10n, C6 100n, C8 27p, C9 27p, C10 100n, C11 10uF, C12 100n, C21, C22, C23, C24, C30 100n, C31 1u, C32 1u, C33 1u, C34 1u
- L1 47uH
- Q1 6 MHz
- D1 SMBJ5.0A
- LB1, LB2, LB3, LB4, LB5, LB6, LB7, LB8
- SW1 RESET
- J1 USBBB, J2 AKS-2, J6 G-3
- VDD, GND, V5, V33

J6 — G-3: DPOM 1, 2, 3

J2 — AKS-2: V5 2, +, 1, -

Signal labels: BAUD0, BAUD1, BAUD2, ADR0, ADR1, ADR2, CLK, SDA, SCL, TXD5, RXD5, USBM, USBP, USBDM, USBDP, RSO, XIN, XOUT, DPOM, RESET, TXD, RTS, RXD, CTS, B+, B-